

R6

Intelligent voice interaction solution

The most cost-effective intelligent dual-mic voice interaction chip

Overview

The R6 integrates ARM9 architecture that operates at speed up to 600MHz. A 32MB DDR is embedded in the R6. It realizes complete voice function under limited cost, and supports mixed interaction between off-line local command word recognition and on-line cloud ASR recognition. The R6 includes rich interfaces such as ADC, DAC, I2S/PCM, USB OTG, SD/MMC, UART, SPI, TWI, etc. Besides, the R6 supports TINA OS. The R6 is qualified to industrial standard in white electricity industry.

Highlights



Dual-mic middle-near field intelligent voice interaction

The R6 integrates ADC, DAC, and I2S/TDM interface, which meets the basic computing rate requirements of dual-mic noise reduction, AEC, wake-up, local command word algorithm and cloud ASR, IOT interaction. The R6 is an ideal choice of middle-near field voice interaction solution.



Low cost, low power consumption, easy to develop

The chip is highly integrated, the BOM development cost is low, and the development is simple.

Features

CPU	<ul style="list-style-type: none"> • ARM9 CPU architecture • 16KByte D-Cache • 32KByte I-Cache
Memory	<ul style="list-style-type: none"> • SIP 32MB DDR • SD2.0, eMMC 4.41
Video	<ul style="list-style-type: none"> • H.264 1280x720@30fps decoding • MPEG1/2/4 1280x720@30fps decoding • MJPEG 1280x720@30fps encoding • JPEG encode size up to 8192x8192
Camera	<ul style="list-style-type: none"> • 8-bit CMOS-sensor interface, supports CCIR656 protocol for NTSC and PAL • TV CVBS input, supports NTSC/PAL

